FIG 1

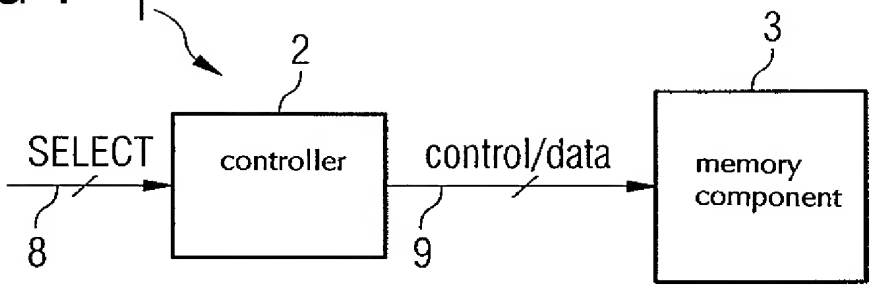

FIG 2

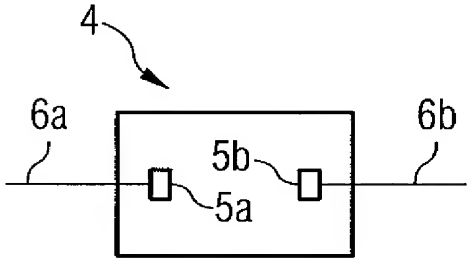

FIG 3

SELECT-signal	memory mode
01	Soft writing
10	Non-volatile writing
11	Hard writing
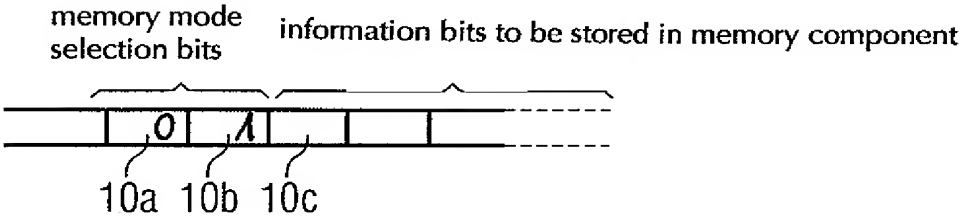
FIG 4